

#### Description

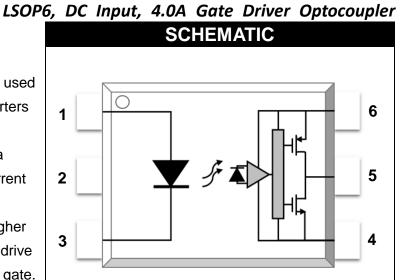
The MPCS-343 series Photocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications and inverters in power supply system. It contains an LED optically coupled to an integrated circuit with a power output stage. The 4.0A peak output current is capable of directly driving most IGBTs with ratings up to 1200 V/200 A. For IGBTs with higher ratings, the MPCS-343 series can be used to drive a discrete power stage which drives the IGBT gate.

#### Features

- 4.0 A maximum peak output current
- Rail-to-rail output voltage
- 110 ns maximum propagation delay
- Under Voltage Lock-Out protection (UVLO) with hysteresis
- Wide operating range: 15 to 30 Volts (V<sub>cc</sub>)
- Guaranteed performance over temperature -40°C ~ +110°C.
- Regulatory Approvals
  - UL UL1577
  - VDE EN60747-5-5(VDE0884-5)
  - CQC GB4943.1, GB8898

#### Applications

- IGBT/MOSFET gate drive
- Uninterruptible power supply (UPS)
- Industrial Inverter
- AC/Brushless DC motor drives
- Switching power suppliers



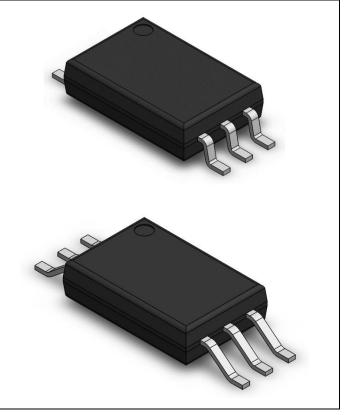
#### PIN DEFINITION

1. Anode 6. V<sub>cc</sub>

2. NC

- 5. Vo
- 3. Cathode 4. GND

#### PACKAGE OUTLINE



Release Date: 2024/4/30

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#### LSOP6, DC Input, 4.0A Gate Driver Optocoupler

TRUTH TABLE						
LED	V <sub>cc</sub> -V <sub>ss</sub>	V <sub>cc</sub> -V <sub>ss</sub>	VO			
LED	(Turn-ON, +ve going)	(Turn-ON, +ve going) (Turn-OFF, -ve going)				
OFF	0 - 30 V	0 - 30 V	Low			
ON	0 - 11.0 V	0 - 9.5 V	Low			
ON	11.0 - 13.5 V	9.5 - 12 V	Transition			
ON	13.5 - 30 V	12 - 30 V	High			

Note: A  $0.1\mu$ F bypass capacitor must be connected between Pin 4 and 6.

ABS	OLUTE MA	XIMUM R	ATINGS		
PARAMETER	SYMBOL	MIN.	MAX.	UNIT	NOTE
Storage Temperature	T <sub>stg</sub>	-55	125	°C	-
Operating Temperature	T <sub>opr</sub>	-40	110	°C	-
Output IC Junction Temperature	TJ	-	125	°C	-
Total Output Supply Voltage	(Vcc –Vss)	0	35	V	-
Average Forward Input Current	lF	-	20	mA	-
Reverse Input Voltage	V <sub>R</sub>	-	5	V	-
"High" Peak Output Current	IOH(PEAK)	-	4.0	А	1
"Low" Peak Output Current	IOL(PEAK)	-	4.0	А	1
Output Voltage	V <sub>O(PEAK)</sub>	-0.5	Vcc	V	-
Power Dissipation	Ρı	-	45	mW	-
Output IC Power Dissipation	Po	-	700	mW	-
Lead Solder Temperature	T <sub>sol</sub>	-	260	°C	-

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Note 1: Exponential waveform. Pulse width  $\leq$  10 µs, f  $\leq$  15 kHz

RECOMMENDED OPERATION CONDITIONS							
PARAMETER	SYMBOL	MIN.	MAX.	UNIT			
Operating Temperature	TA	-40	110	°C			
Supply Voltage	V <sub>CC</sub>	15	30	V			
Input Current (ON)	I <sub>F(ON)</sub>	5	16	mA			
Input Voltage (OFF)	VF(OFF)	-3.0	0.8	V			



			-	-	-	A Gate Driver Optoc	oupler
PARAMETER	SYMBOL	MIN.	TYP.	MAX.		ISTICS TEST CONDITION	NOTE
		INPU		CTERISTI	CS		
Input Forward Voltage	VF	1.6	1.9	2.4	V	IF=10mA	-
Input Forward Voltage Temperature Coefficient	Δν <sub>γ</sub> / Δτ	-	-1.237	-	mV/°C	IF=10mA	-
Input Reverse Voltage	BV <sub>R</sub>	5	-	-	V	IR = 10µA	-
Input Threshold Current (Low to High)	I <sub>FLH</sub>	-	0.9	2	mA	V <sub>O</sub> > 5V, I <sub>O</sub> = 0A	-
Input Threshold Voltage (High to Low)	Vfhl	0.8	-	-	V	VCC = 30 V, VO < 5V	-
Input Capacitance	CIN	-	60	-	pF	f = 1 MHz, VF = 0 V	-
		OUTP	UT CHAR	ACTERIST	ICS		
High Level Supply Current	Іссн	-	1.70	3	mA	I <sub>F</sub> = 10 mA, VCC = 30V, VO = Open	-
Low Level Supply Current	I <sub>CCL</sub>	-	2.11	3	mA	I <sub>F</sub> = 0 mA, VCC = 30V, VO = Open	-
High level output current	Іон	4.0	-	-	A	I <sub>F</sub> = 10 mA, VCC = 30V VO = VCC - 15	1
Low level output current	Iol	4.0	-	-	A	I <sub>F</sub> = 0 mA, VCC = 30V VO = VSS + 15	1
High level output voltage	Vон	29.7	29.88	-	V	IF = 10mA, IO = -100mA	2,3
Low level output voltage	Vol	-	0.1	0.3	V	I <sub>F</sub> = 0 mA, IO = 100 mA	-
UVLO Threshold	Vuvlo+	11.0	12.6	13.5	V	VO > 5V, IF = 10 mA	-
	Vuvlo-	9.5	11.2	12.0	V	VO < 5V, IF = 10 mA	-

Note: All Typical values at  $T_A = 25^{\circ}$ C and  $V_{CC} - V_{SS} = 30$  V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Maximum pulse width = 10  $\mu$ s.

Note 2: In this test VOH is measured with a dc load current. When driving capacitive loads, VOH will approach VCC as IOH approaches zero amps.

Note 3: Maximum pulse width = 1 ms.



ISOP6 DC Input, 4.04 Gate Driver Ontocounler

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PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
Propagation Delay Time to Low Output Level	<b>t</b> PLH	-	74.5	110			-
Propagation Delay Time to High Output Level	t <sub>PHL</sub>	-	61.3	110		$Rg = 10 \Omega,$ $Cg = 25 nF,$	-
Pulse Width Distortion	Pwd	-	22	70	ns	f = 10 kHz,	-
Propagation Delay Difference Between Any Two Parts	Pdd (tphl - tplh)	-100	-	+100		Duty Cycle = 50% $I_F = 10mA$ , $V_{CC} = 30V$	-
Output Rise Time (20 to 80%)	tr	-	20	-	-	$v_{CC} = 30v$	-
Output Fall Time (80 to 20%)	t <sub>f</sub>	-	15	-			-
Common mode transient immunity at high level output	CM <sub>H</sub>	20	40	-	kV/µs	IF= 7 to 16mA $V_{CC}$ = 30V, $T_{A}$ = 25 °C, $V_{CM}$ = 1kV	1,2
Common mode transient immunity at low level output	CM∟	20	40	-	kV/µs	IF=0mA Vcc= 30V, T <sub>A</sub> = 25 °C, V <sub>CM</sub> = 1kV	1,3

Note: All Typical values at TA =  $25^{\circ}$ C and V<sub>CC</sub> – V<sub>SS</sub> = 30 V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Pin 2 needs to be connected to LED common.

Note 2: Common mode transient immunity in the high state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in the high state (meaning VO > 15.0V). Note 3: Common mode transient immunity in a low state is the maximum tolerable dVCM/dt of the common mode pulse, VCM, to assure that the output will remain in a low state (meaning VO < 1.0V).



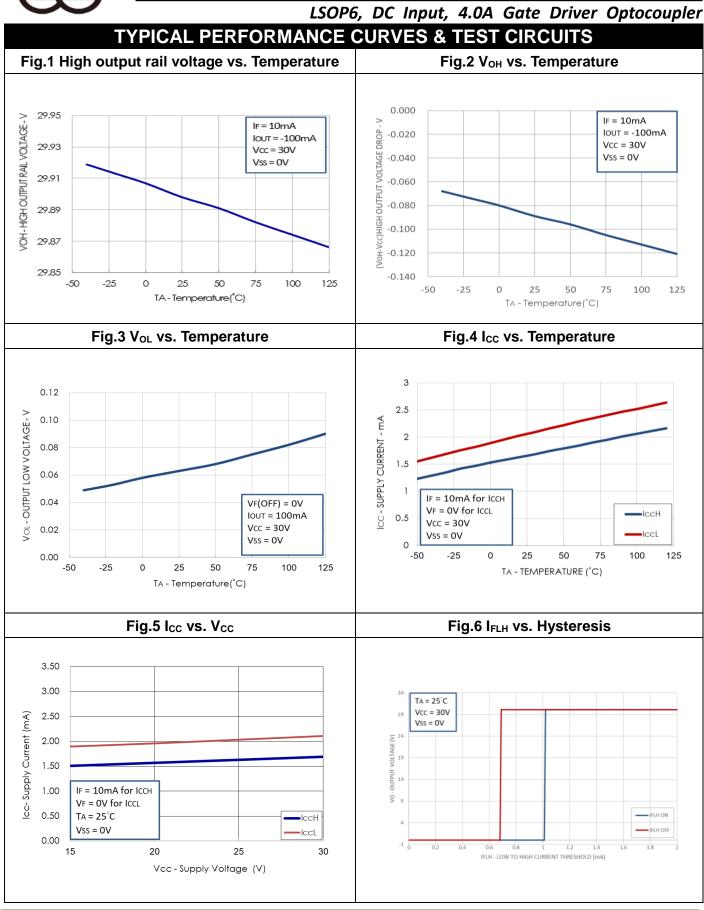
$\mathbf{U}$		LS	ОР6,	DC In	put, 4	.0A G	ate Driver Optoc	oupler
ISOLATION CHARACTERISTIC								
PARAMETER	SYMBOL	DEVICE	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
Withstand Insulation	Viso	MPCS-343P	5000			V	RH ≤ 40%-60%,	10
Test Voltage	VISO	MPCS-343W	5000	-	-	v	t = 1min, $T_A = 25 \ ^\circ C$	1,2
Input-Output	Ri-o			10 <sup>12</sup>		Ω	V <sub>I-0</sub> = 500V DC	1
Resistance	NI-0	-	-	10	-	12	$v_{1-0} = 500 v DC$	

Note: All Typical values at  $T_A = 25^{\circ}$ C and  $V_{CC} - V_{SS} = 30$  V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Device is considered a two terminal device: pins 1, 2, 3 are shorted together and pins 4, 5, 6 are shorted together.

Note 2: According to UL1577, each photocoupler is tested by applying an insulation test voltage 6000VRMS for one second. This test is performed before the 100% production test for partial discharge.



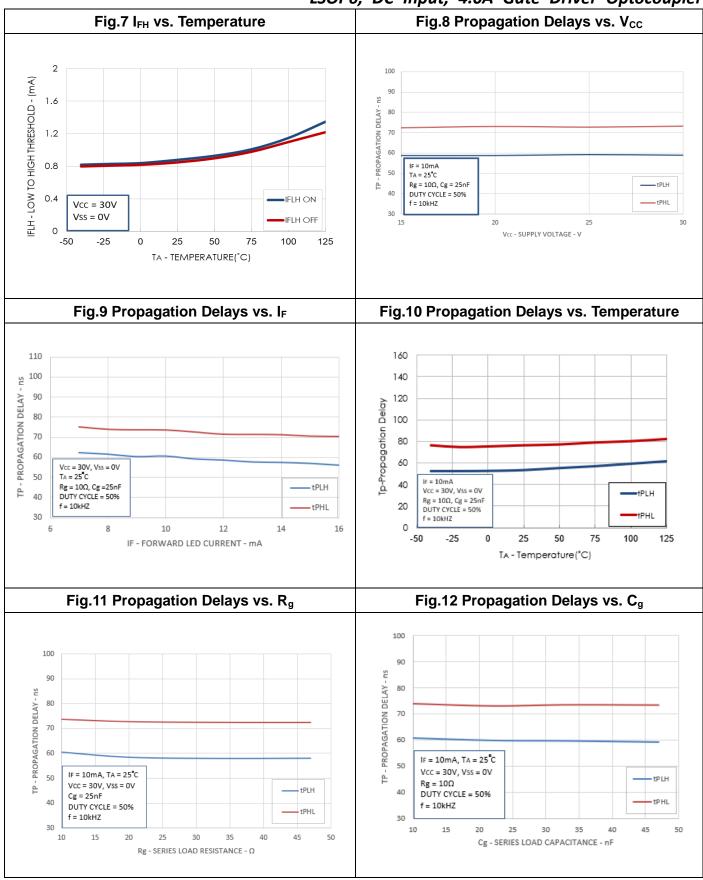


Rev: 2.0

Release Date: 2024/4/30



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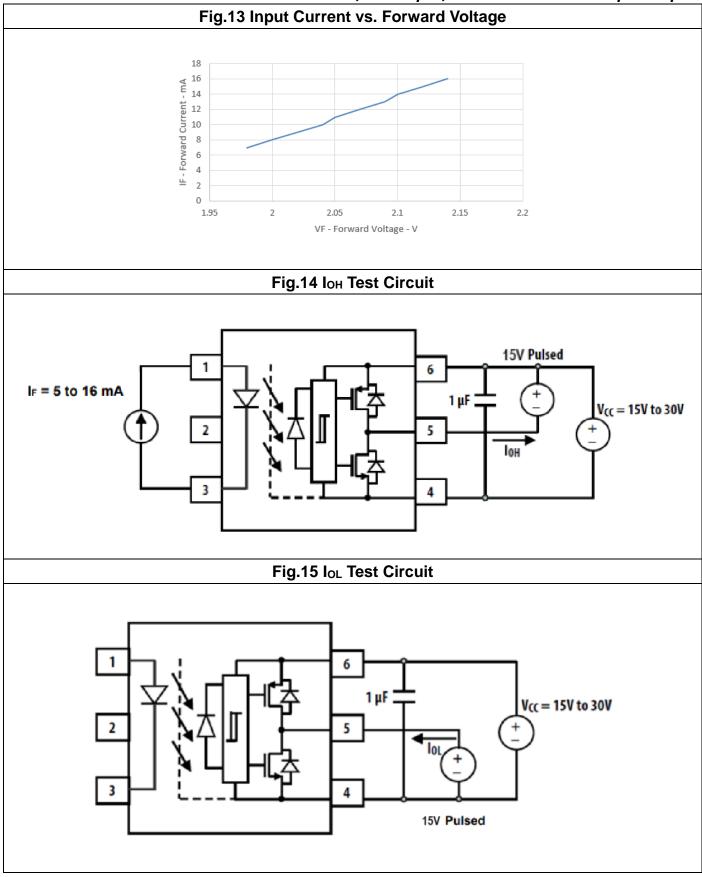


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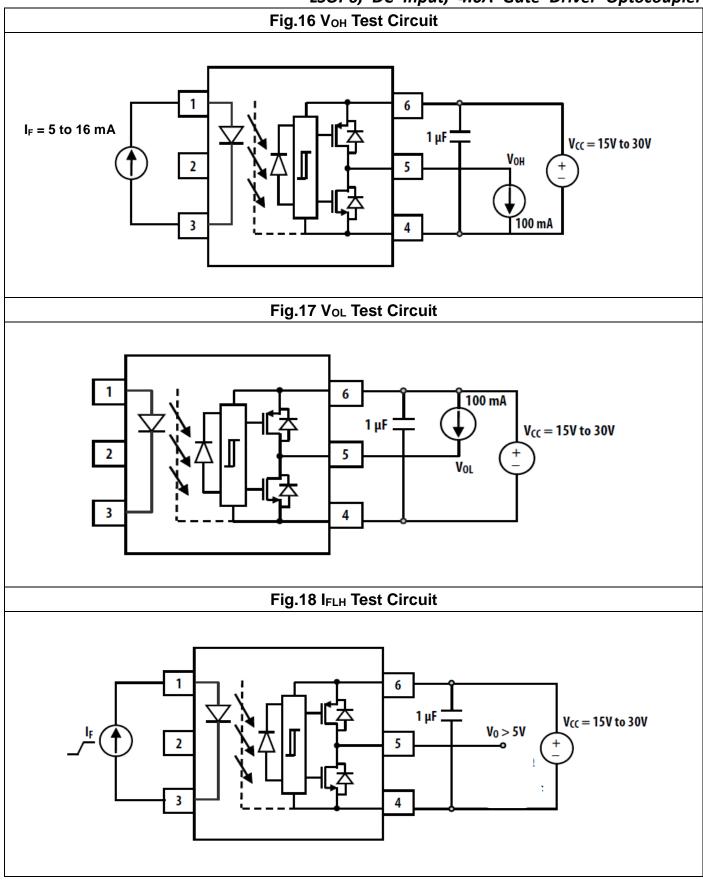




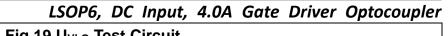


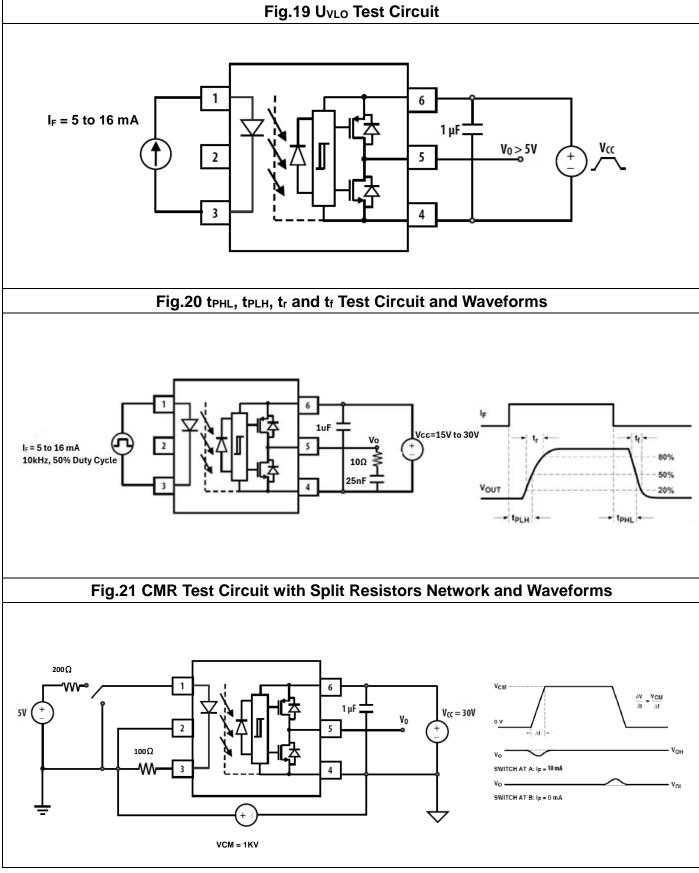




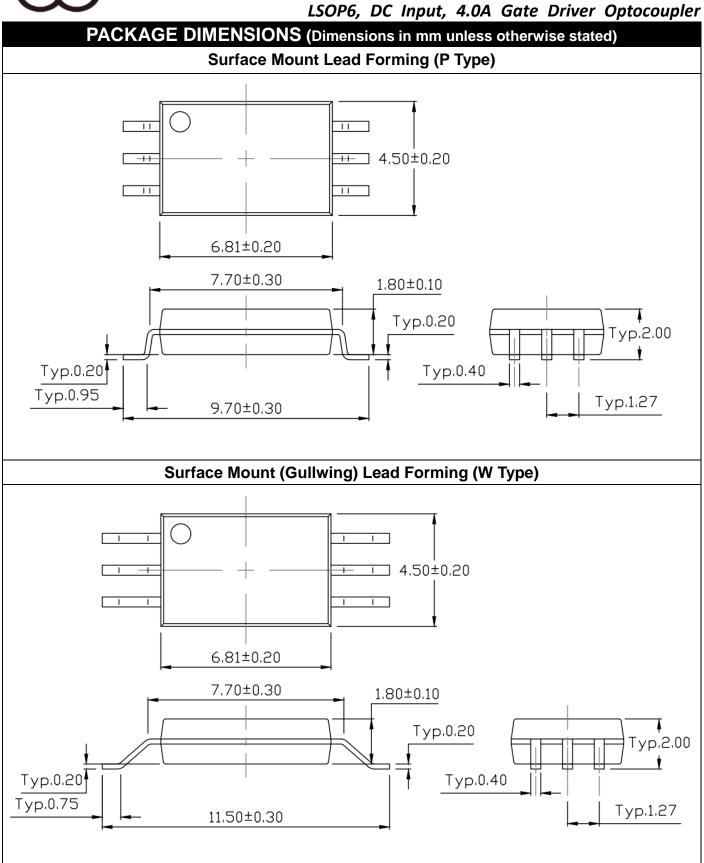




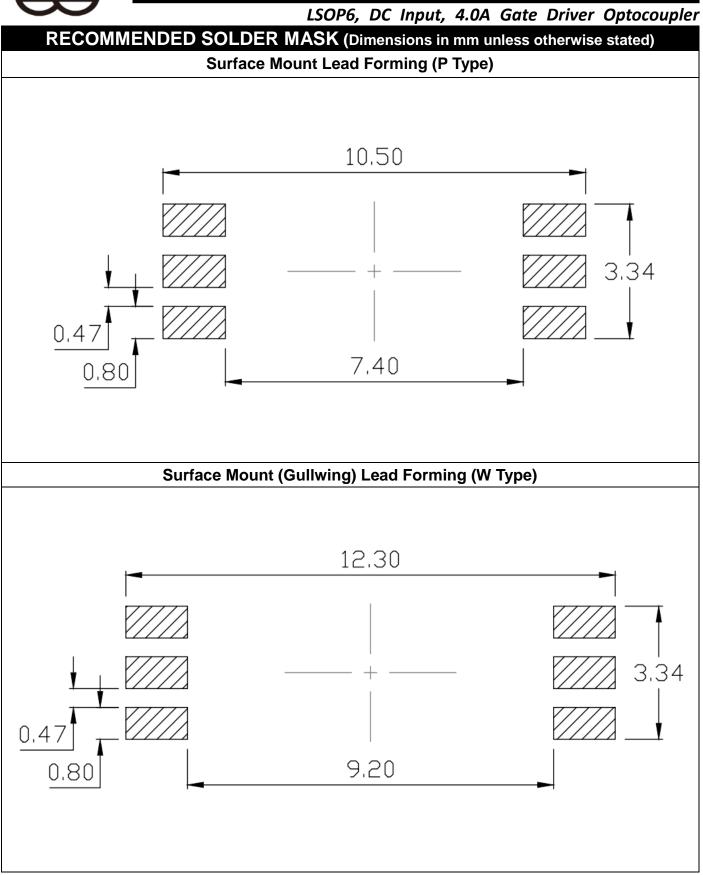




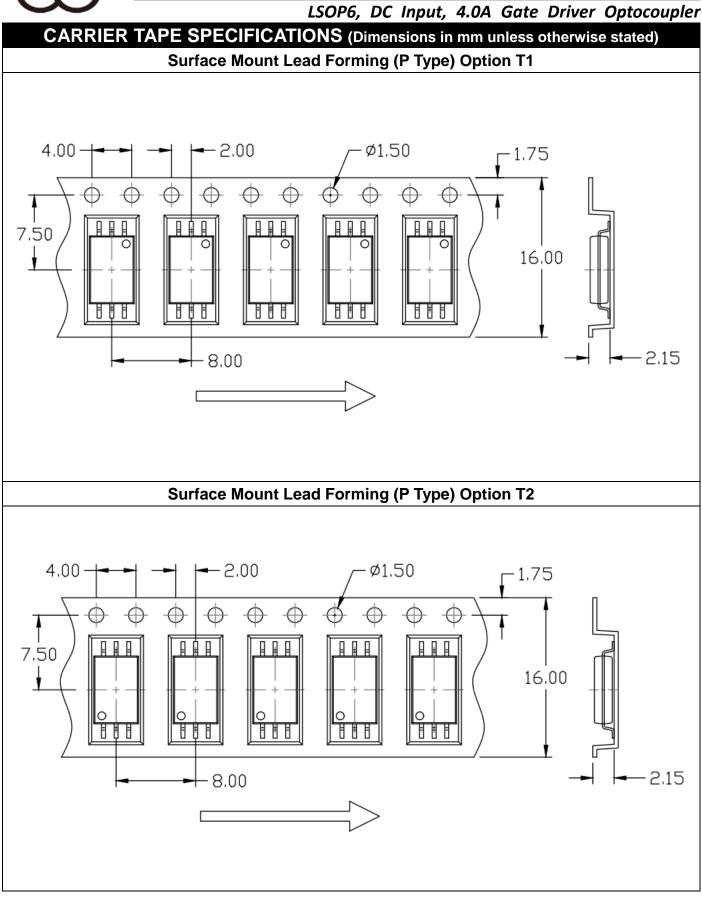




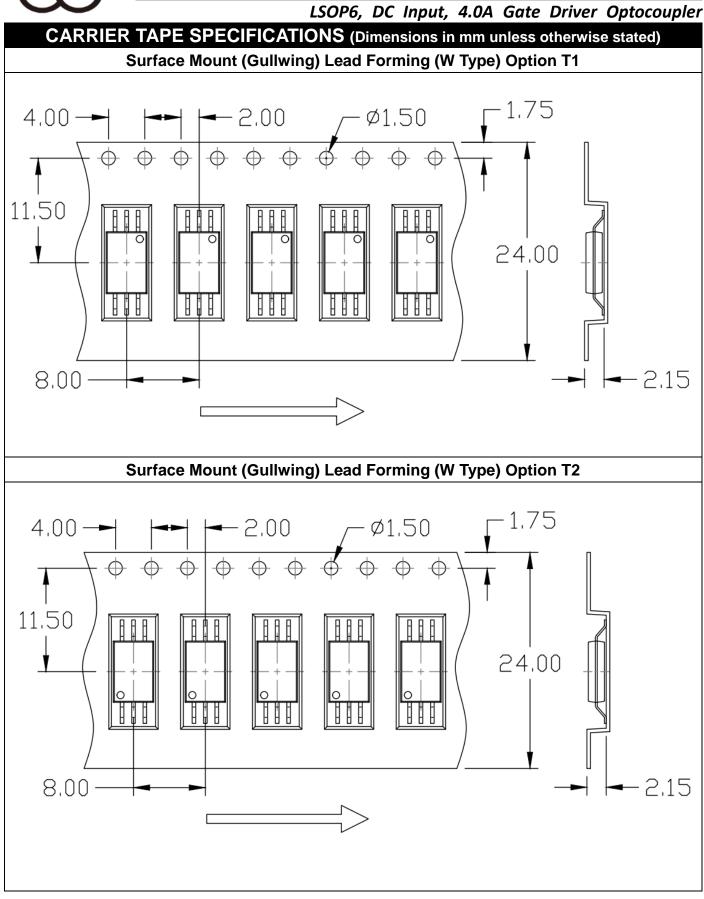






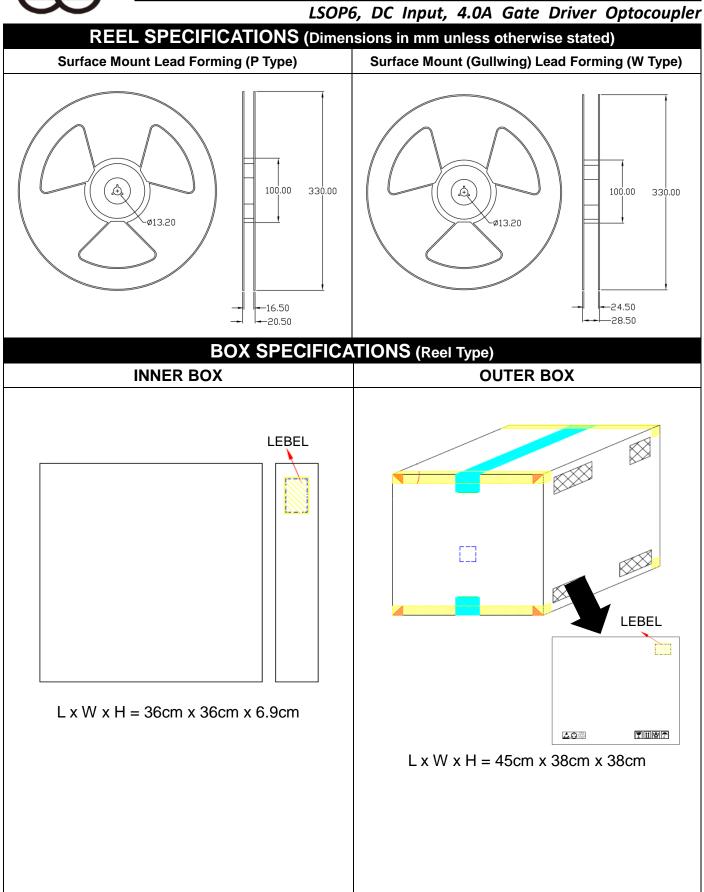






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Option W T1/T2

3000 Units/Reel

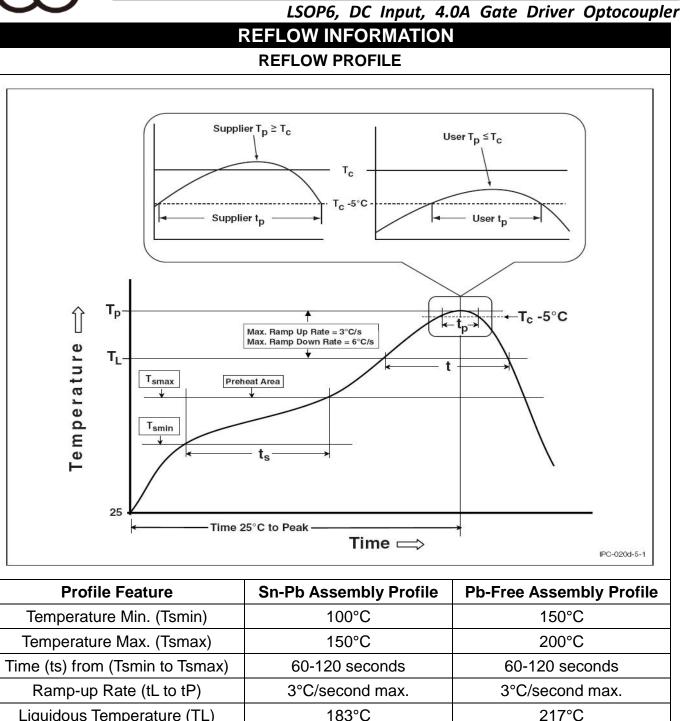
## MPCS-343 Series

	LSOP6, DC Input, 4.0A Gate Driver Optocoupler						
ORDERING AND MARKING INFORMATION							
MARKING INFORMATION							
	MYYWW 343 TV		M YY WW 343 T or H V	: Company Abbr. : Year date code : 2-digit work week : Part Number I : Factory identification mark : VDE Identification(Option)			
ORD	DERING INFORMAT	ION		LABEL INFORMATION			
MPC	CS-343(P/W)	-ZV		盐光照明光電股份有限公司			
MPC – Company Abbr. S – Stack 343 – Part Number P/W – Lead Form Option (P-9mm Clearance or W-11mm Clearance) Z – Tape and Reel Option (T1/T2) V –VDE Option (V or None)			Lot Date Q'ty	WISELITE Optronics Co., Ltd No : XXXXXXXXXXXX Bin Code : X No : XXXXXXXXXXX e Code : XXXX : XXXX pcs			
PACKING QUANTITY							
Option	Quantity	Quantity – Inner	box	Quantity – Outer box			
Option P T1/T2	3000 Units/Reel	3 Reels/Inner b	ох	5 Inner box/Outer box = 45k Units			

5 Inner box/Outer box = 30k Units

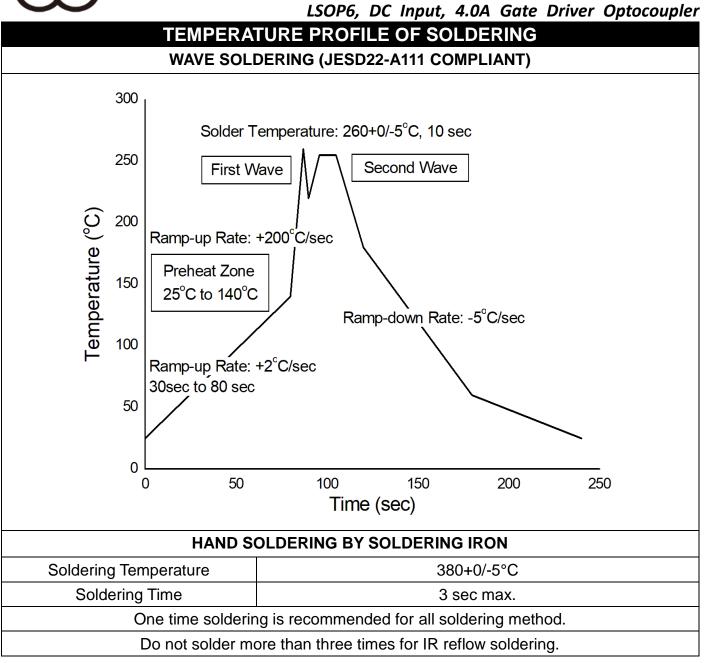
2 Reels/Inner box





Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (tP) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.







#### LSOP6, DC Input, 4.0A Gate Driver Optocoupler DISCLAIMER

- WISELITE is continually improving the quality, reliability, function and design. WISELITE reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
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- This product is not intended to be used for military, aircraft, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact WISELITE sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify WISELITE's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.